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AlGa_N channel high electron mobility transistors on bulk AlN substrate

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Nowadays, AlGa_N/Ga_N HEMTs have become the sailor of the next generation of power devices delivering promising electrical performance as compared to conventional silicon based power devices. Novel HEMTs based on the Al_xGa_{1-x}N channel are becoming the subject of interest for pushing the performance of power devices beyond the limitations of Ga_N devices [1]. In this work, we report on the first electrical operation of AlGa_N channel HEMTs grown on bulk AlN substrate. The Al_{0.23}Ga_{0.77}N channel HEMT showed remarkable transistor breakdown voltage > 2100V for large gate-drain spacing. In contrast, a significantly lower breakdown voltage is observed in HEMT-on-AlN using various Ga_N channel thicknesses. Lastly, we show a short-term robustness test comparison of Ga_N and AlGa_N channel devices under a high electric field.

The Al_{0.60}Ga_{0.40}N /Al_{0.23}Ga_{0.77}N and Al_{0.30}Ga_{0.70}N /Ga_N heterostructures were grown by ammonia source MBE on 2" bulk AlN substrates. The three different heterostructures were realized with 200nm Ga_N channel (Sample_A), 100nm Ga_N channel (Sample_B), and 500nm thick AlGa_N channel. The active area of devices was mesa isolated down to the bulk AlN. MIS (Metal Insulator Semiconductor)-HEMTs devices were fabricated by deposition of 30nm PECVD Si₃N₄ before e-beam evaporation of 3μm Ni/Au gates, with a 2×50 μm width. The DC transfer characteristics of HEMTs show fully functional devices with low-off state leakage current (about 1μA/mm) and good on-state performance (see Fig.2). The output DC characteristics yield a maximum drain current of I_d=900 mA/mm for Sample_A, I_d=173 mA/mm for Sample_B, and I_d=280 mA/mm for Sample_C as shown in Fig. 3. This concurs with the sheet resistances but it is limited by the non-optimized ohmic contacts. Buffer breakdown characteristics reveals an excellent quality of AlN substrate showing a breakdown field up to 9MV/cm for low contact spacing and a buffer breakdown voltage as high as 3600V for a contact spacing of 31 μm defined at 1 μA/mm. The high voltage characteristics of Al_{0.23}Ga_{0.77}N channel transistors outperform Ga_N channel devices with an off-state breakdown voltage of 2189 V for GD = 40μm against less than 500 V, respectively. A robustness test comprises 20 times 1500V off-state sweeps realized on Sample_B and Sample_C in order to evaluate their performance under a high electric field. For Ga_N channel device the off state, leakage current increases by two folds after stress while AlGa_N channel device showed no change in leakage current. The threshold voltage shift is ascribed to trapping effects.

References

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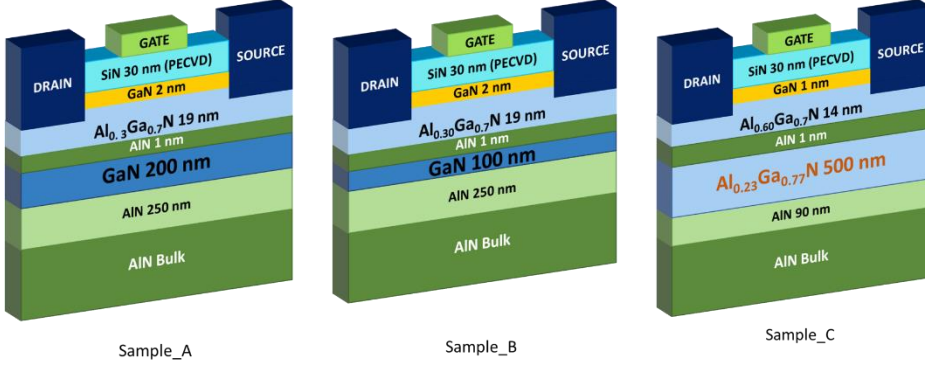


Fig. 1 Schematic diagram of GaN and AlGaN channel HEMTs

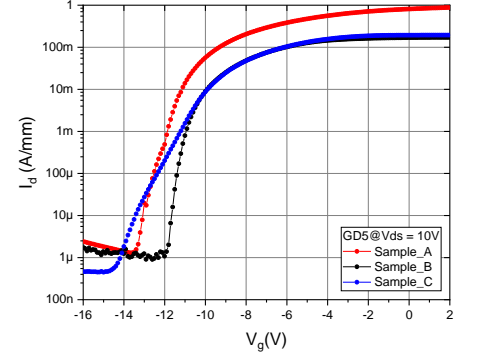


Fig. 2 I_d - V_g characteristics

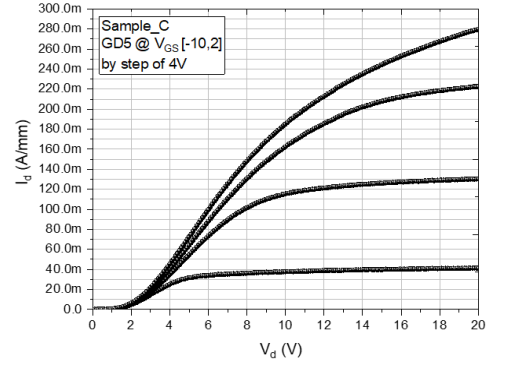
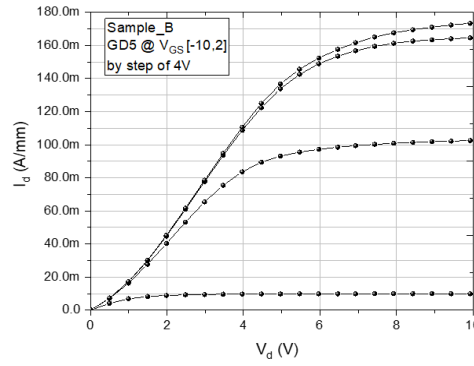
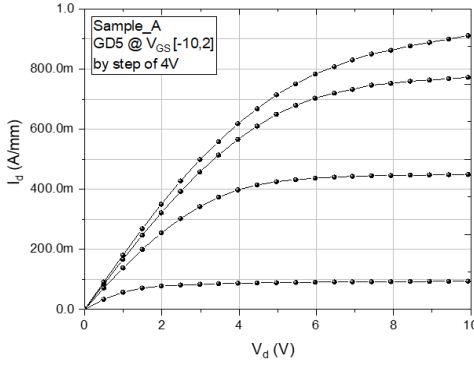


Fig. 3 I_d - V_d characteristics of Sample_A (left), Sample_B (middle) and Sample_C (right) for gate-drain spacing of $5\mu\text{m}$.

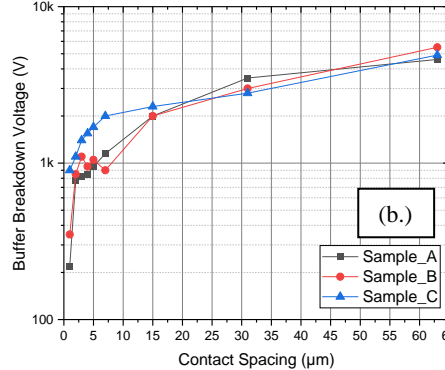
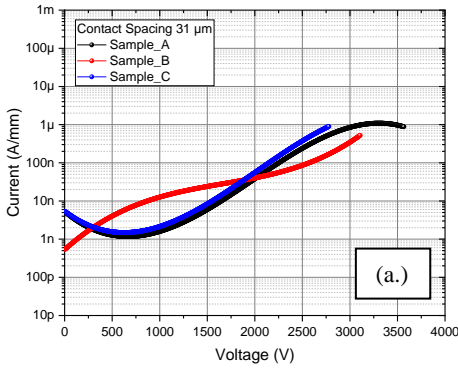


Fig. 4 (a.) Buffer breakdown characteristics for $31\mu\text{m}$ contact spacing, (b.) buffer breakdown voltage vs two terminal contact spacing (right)

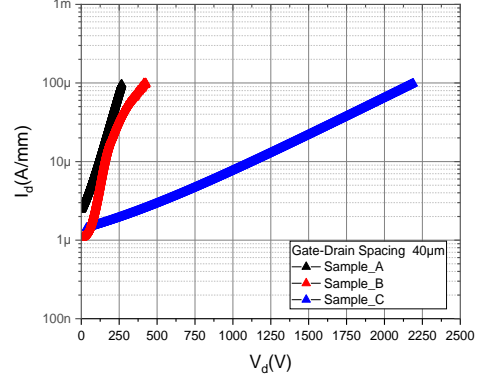


Fig. 5 (a.) Transistor Breakdown I-V characteristics for transistor with $40\mu\text{m}$ gate-drain spacing

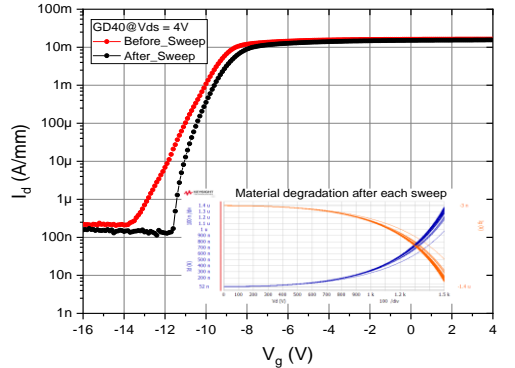
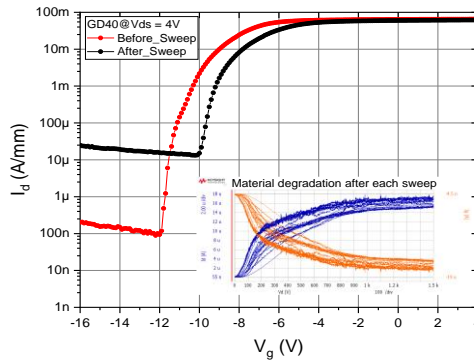
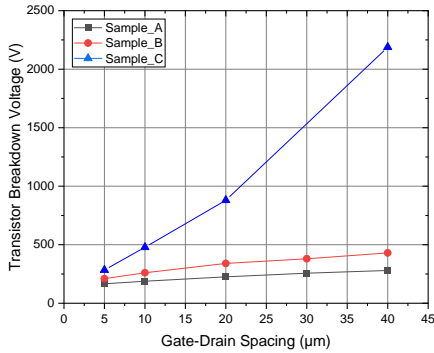


Fig. 5 (b.) Transistor Breakdown Voltage realised at $100\mu\text{A/mm}$ leakage current vs gate-drain spacing

Fig. 6 I_d - V_g characteristics of Sample_B (left) and Sample_C (right)